

**Amendments to the Specification:**

Please replace the first paragraph of the specification with the following paragraph:

--The present Application is a divisional of Application No. 09/693,709, filed October [[2]]  
20, 2000, which claims the benefit from provisional Application 60/160,501, filed on 10/20/99.--

Please replace the paragraph at page 25, lines 12-16 (paragraph 90 in the published application) with the following paragraph:

--FIG. 11 is a block diagram illustrating a modification of the alternate transmitter of FIG. 4 to incorporate trellis coding. In FIG. 11 a ~~5/4~~ 4/5 convolutional coder accepts a 4 bit input and provides a 5 bit output. The addition of a convolutional encoder provides redundancy in the code and provides a mechanism for reducing problems such as inter-symbol interference.—

Please replace the paragraph at page 25, lines 30-34 (paragraph 92 in the published application) with the following paragraph:

--FIG. 13 is a block diagram illustrating a modification of the receiver of FIG. 4 to receive a trellis coded signal. In FIG. 13 the 32 ~~Inverse~~ Fourier Transform provides symbols (S) along with the complex conjugate of the signals (S\*) to a trellis decoder. The trellis decoder decodes the symbols and then provides a 4 bit output signal to the XGMII to combine with other signals in order to produce the output signal.--